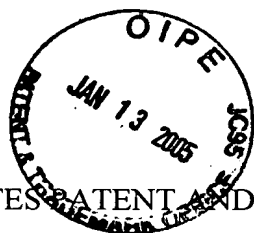
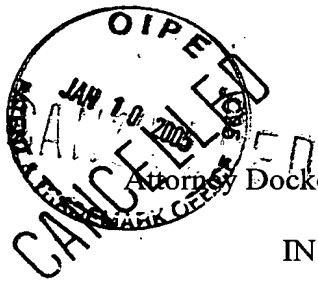


IFW



Attorney Docket No. 5649-1202

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Su-Jin Ahn et al.  
Serial No.: 10/696,615  
Filed: October 28, 2003  
For: SEMICONDUCTOR MEMORY DEVICES HAVING OFFSET TRANSISTORS  
AND METHODS OF FABRICATING THE SAME

Group Art Unit: 2811  
Examiner: Sara W. Crane  
Confirmation No.: 6846

January 11, 2005

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT AND  
RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to the Restriction Requirement of December 15, 2004, Applicants hereby elect the invention of **Invention I** which is drawn to Claims 1-21.

Applicants are amending the Title to reflect the above-identified election.

It is not believed that an extension of time and/or additional fee(s), including fees for net addition of claims-are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

In re: Su-Jin Ahn et al.  
Serial No.: 10/696,615  
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Page 2 of 8

**In the Title:**

Please delete the Title in all occurrences and substitute the following Title therefor:

--SEMICONDUCTOR MEMORY DEVICES HAVING OFFSET TRANSISTORS--.